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CT Receipt

Docket No. 740819-559

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | |
|---|---|---|
| In re Patent Application of: | ) | |
| Shin HASHIMOTO et al | ) | Group Art Unit: 1765 |
| Serial No. 09/856,823 | ) | Examiner: Unknown |
| Filed: May 25, 2001 | ) | |
| For: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE | ) | Date: November 5, 2002 |

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231, on November 5, 2002.

Bonnie L. Garilli

Name: Bonnie L. Garilli

**STATUS INQUIRY OF**

**CORRECTED FILING RECEIPT**

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DEC 09 2002
TC 1700

Commissioner for Patents
Washington, D.C. 20231

Sir:

Please advise, in writing, as to the current status of the corrected Filing Receipt (copy attached).

Respectfully submitted,

T Thomas W Cole

Thomas W. Cole
Registration No. 28,290

NIXON PEABODY LLP
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This will acknowledge receipt of the following:

1. Transmittal Form
2. Request for Corrected Filing Receipt
3. Request for Consideration of Documents Cited in International Search Report

In re PATENT application of:

SHIN HASHIMOTO et al
Serial No. 09/856,823
Filed: 05/25/2001
Due Date: 11/25/2002
For: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE

Docket: 740819-000559
October 19, 2001
EJR/EJR/amd

Hand Delivery

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OCT 19 2001
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OCT 22 2001
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10/23/01

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NOV 13 2002
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Docket No. 0819-0559

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of )
Shin HASIMATO et al. ) Attn: Customer Correction Branch
Serial No. 09/856,823 )
Filed: May 25, 2001 )
For: METHOD FOR FABRICATING )
SEMICONDUCTOR DEVICE )

Attention: Application Processing Division

**REQUEST FOR CORRECTED FILING RECEIPT**

Honorable Commissioner of Patents
Washington, D.C. 20231

Sir:

The Filing Receipt received in the above-identified application has errors thereon due to Patent Office error as shown in red on the attached copy.

In view of the above, it is requested that a Corrected Filing Receipt be issued.

Respectfully submitted,

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UNITED STATES PATENT AND TRADEMARK OFFICE

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UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
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| APPLICATION NUMBER | FILING DATE | GRP ART UNIT | FIL FEE REC'D | ATTY.DOCKET.NO | DRAWINGS | TOT CLAIMS | IND CLAIMS |
|---|---|---|---|---|---|---|---|
| 09/856,823 | ~~05/29/2001~~ 05/25/2001 | 1765 | 1130 | 0819-559 | 8 | 16 | 2 |

**CONFIRMATION NO. 3480**

**FILING RECEIPT**

*OC000000006266991*

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Date Mailed: 07/06/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

**Applicant(s)**

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Tomoya Tanaka, Kyoto, JAPAN;

**Domestic Priority data as claimed by applicant**

THIS APPLICATION IS A 371 OF PCT/JP00/06851 10/03/2000

**Foreign Applications**

JAPAN 11-282668 10/04/1999
JAPAN 2000-263467 08/31/2000

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**Projected Publication Date:** N/A

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Method ~~of manufacturing~~ for fabricating semiconductor device